

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1519	(raster\$5 print\$3) near2 data with column	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:18
L2	1609	358/1.16.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:13
L3	66	1 and 2	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:13
L4	224	raster\$5 near8 memory with column	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:21
L5	6	2 and 4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:18
L6	181	4 and @ad<"20020201"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:21
S1	0	0153877b1	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 12:47
S2	4	"0153877"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/26 15:11
S3	1	"60270685"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/26 15:12
S4	1467	(raster\$5 print\$3) near2 data with column	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/16 11:12
S5	540	S4 with (RAM memory disk stor\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:35
S6	0	(raster\$5 print\$3) near2 data with "column by column"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 12:49
S7	38	(raster\$5 print\$3) near2 data with column near1 column	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 13:04
S8	0	raster adj image adj processor with column near1 column	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 13:05
S9	15	raster adj image adj processor and column near1 column	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 13:05

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S10	1	"4694405".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:07
S11	226	plug\$in with memory with processor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:16
S12	1	S11 with raster	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:08
S13	62073	S11 same raster	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:08
S14	1	S11 same raster	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:09
S15	45	plug\$in near2 board with memory with processor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:21
S16	45440	"358"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:16
S17	8	S11 and S16	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:16
S18	164	(plug\$in expansion)near2 board with memory with processor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:21
S19	164	(plug\$in expansion) near2 board with memory with processor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:22
S20	164	(plug\$in or expansion) near2 board with memory with processor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:22
S21	1	S16 and S20	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:22
S22	1	S20 same printing	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 18:23
S23	27	S20 same pixel	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:28
S24	1497	358/1.16.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:28

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S25	65	S4 and S24	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:34
S26	36681	dma:near1controller	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:35
S27	10052	dma near1 controller	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:35
S28	74	S4 and S27	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:36
S29	74	S28 and(RAM memory disk stor\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:36
S30	74	S28 and (RAM memory disk stor\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/27 19:37